In the Specification

Page 31, please replace lines 12-23, with the following amended paragraph:

-- Next, the case that all digital audio data of at least one of the section 1 to the section 7 has been read will be described. At step S10, since the address register for each section that has been read stores the address of the end section, the address of the beginning section is restored to the address register. At step S11, the bit Bn of the flag data corresponding to each section that has been read is set to "1". In other words, since the flag data is used for the next frame, when one of the bits B_1 to B_7 of the flag data is 1, the beginning data of the section corresponding to the bit B_n that is 1 is placed in the ~~flame~~ frame.--

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